# Clock OSC

## SG-310SCN

## Product name SG-310SCN 50.000000 MHz B Product Number / Ordering code Q33310N700034xx

Please refer to the 8.Packing information about xx (last 2 digits)

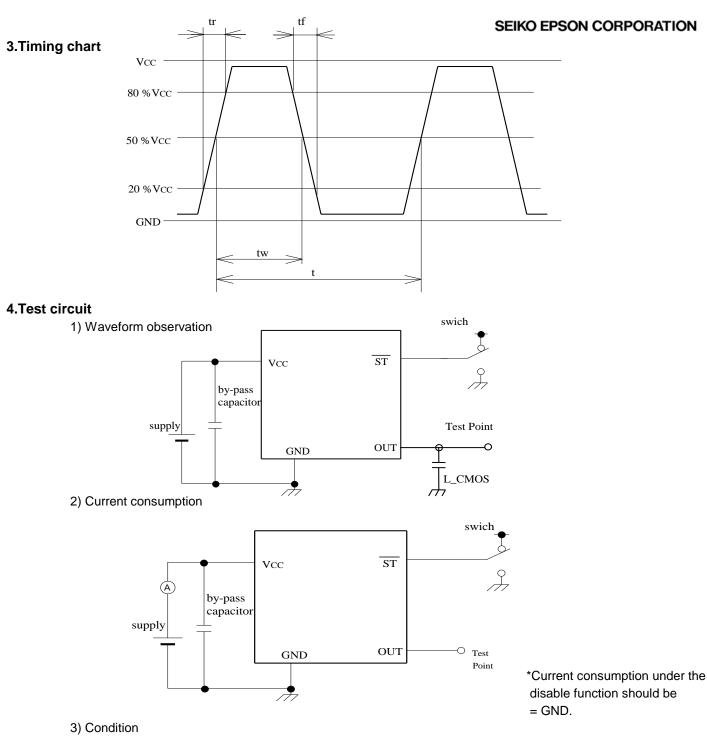
## Output waveform CMOS

Pb free / Complies with EU RoHS directive

Reference weight Typ. 26 mg	
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1.Absolute maximum ratings						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Maximum supply voltage	Vcc-GND	-0.3	-	7	V	-
Storage temperature	T_stg	-40	-	+125	°C	Storage as single product
Input voltage	Vin	-0.3	-	Vcc+0.3	V	ST terminal

2.Specifications(characteris	tics)					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions / Remarks
Output frequency	fO		50.000000		MHz	
Supply voltage	Vcc	2.7	3.3	3.6	V	-
Operating temperature	T_use	-20	-	+70	°C	-
Frequency tolerance	f_tol	-50	-	50	x10 <sup>-6</sup>	T_use
Current consumption	lcc	-	-	7	mA	No load condition
Stand-by current	I_std	-	-	10.0	μA	ST = GND
Symmetry	SYM	45	-	55	%	50% Vcc Level L_CMOS=<15pF
Output voltage	V <sub>OH</sub>	0.9Vcc	-	-		IOH=-3mA
	V <sub>OL</sub>	-	-	0.1Vcc		IOL=3mA
Output load condition	L_CMOS	-	-	15	pF	CMOS Load
Input voltage	V <sub>IH</sub>	0.7Vcc	-	-		ST terminal
	V <sub>IL</sub>	-	-	0.3Vcc		ST terminal
Rise time	t <sub>r</sub>	-	-	4	ns	0.2Vcc to 0.8Vcc Level, L_CMOS=15pF
Fall time	tf	-	-	4	ns	0.2Vcc to 0.8Vcc Level, L_CMOS=15pF
Start-up time	t_str	-	-	2	ms	t = 0 at 0.9Vcc
Jitter	t <sub>DJ</sub>	-	0	-	ps	Deterministic Jitter Vcc=3.3
	t <sub>RJ</sub>	-	2.4	-	ps	Random Jitter Vcc=3.3V
	t <sub>RMS</sub>	-	2.4	-	ps	$\delta$ (RMS of total distribution) Vcc=3.3V
	t <sub>p-p</sub>	-	22	-	ps	Peak to Peak Vcc=3.3V
	t <sub>acc</sub>	-	5.4	-	ps	Accumulated Jitter(δ) n=2 to 50000 cycles
Phase jitter	t <sub>PJ</sub>	-	0.31	-	ps	Off set Frequency: 12kHz to 20MHz Vcc=3.3V
Phase noise	L(f)	-	-	-	dBc/Hz	-
		-	-83	-	dBc/Hz	Off set 10Hz Vcc=3.3V
		-	-112	-	dBc/Hz	Off set 100Hz Vcc=3.3V
		-	-130	-	dBc/Hz	Off set 1kHz Vcc=3.3V
		-	-139	-	dBc/Hz	Off set 10kHz Vcc=3.3V
		-	-144	-	dBc/Hz	Off set 100kHz Vcc=3.3V
		-	-154	-	dBc/Hz	Off set 1MHz Vcc=3.3V
Frequency aging	f_age	-3	-	3	x10 <sup>-6</sup>	@+25°C first year
		-10	-	10		25℃,10Years



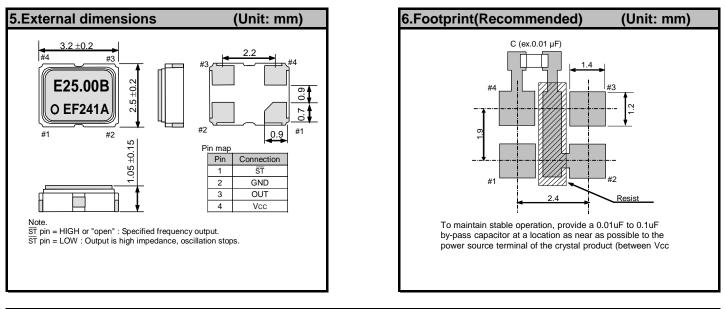
(1) Oscilloscope

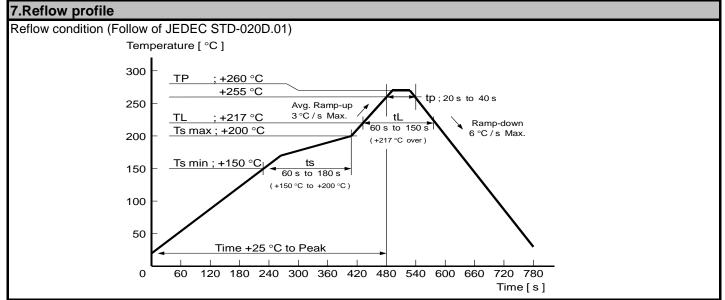
· Band width should be minimum 5 times higher (wider) than measurement frequency.

· Probe earth should be placed closely from test point and lead length should be as short as pssible

- \* Recommendable to use miniature socket. (Don't use earth lead.)
- (2) L\_CMOS also includes probe capacitance.
- (3) By-pass capacitor (0.01  $\mu$ F to 0.1  $\mu$ F) is placed closely between VCC and GND.
- (4) Use the current meter whose internal impedance value is small.
- (5) Power supply
- $\cdot$  Start up time (0 %VCC to 90 %VCC) of power source should be more than 150  $\mu s.$
- $\cdot$  Impedance of power supply should be as lowest as possible.

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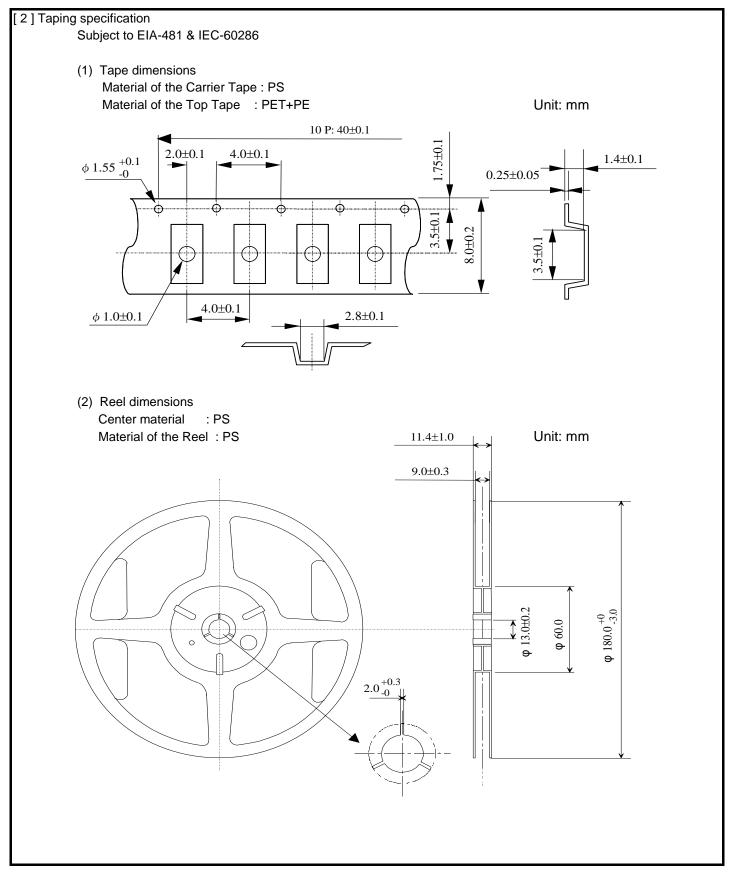




#### 8.Packing information

[ 1 ]Product number last 2 digits code(xx) description			The recommended code is "00"		
	Q33310N	J700034xx			
	Code	Condition	Code	Condition	
	01	Any Q'ty vinyl bag(Tape cut)	13	500pcs / Reel	
	11	Any Q'ty / Reel	14	1000pcs / Reel	
	12	250pcs / Reel	00	2000pcs / Reel	

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- / Traffic control equipment
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